

Name : K. VASANTHA

Designation : GUEST FACULTY

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Area of Interest: COMMUNICATION SYSTEMS

Educational Qualifications:

S.No	Qualification	Specialization	Year of Passed
1	UG	ECE	2005
2	PG	COMMUNICATION SYSTEMS	2010

Experience: (16 Years)

Name of Institutions	Designation	Year of Experience
University college of Engineering Ariyalur, Ariyalur	Teaching Fellow	13Years
Pavendar Bharathidasan College of Engineering & Technology, Trichy	Assistant Professor	5 months 13 days
Sree Sastha College of Engineering, Chennai	Assistant Professor	2 years, 5 months

Additional / Academic Responsibilities at Institution/University Level:

- Naan Mudhalvan – college -staff In-charge
- Class In-charge
- Time table In-charge
- Department Exam cell Co-coordinator
- Sports Co-coordinator
- Project Guide
- Alumni Co-coordinator

Professional Activities:

Acted as Examiner for Central valuation of Anna University Chennai.

Acted as Anna University Representative for University Theory Examinations

Acted as External Examiners for University Theory and Practical Examinations

Acted as Internal Examiners for Anna University Theory and Practical Examinations

Acted as Invigilator for TNPSC examinations.

Acted as Invigilator for NEET PG 2022 examinations

Achievements

- Produced 100% result in the subject VLSI DESIGN for VI semester of 2015-16 batch ECE students
- Produced 100% result in the subject LINEAR INTEGRATED CIRCUITS & ITS APPLICATIONS in 2nd semester of 2013-2014 for EEE students

- **UG/PG Projects Completed: 15**
- **List of Seminar / Short Term Course /FDP/ Workshop attended:**

  - AICTE sponsored Faculty Development Programme on Pedagogy in wireless communication Technologies from 18th to 30th November 2013. At M.A.M. College of Engineering & Technology Trichy 621 105.
  - Faculty Development Programme on Design Techniques of Reconfigurable System on 26th August 2014 at University College of Engineering, Pattukkottai in association with Enixs Technology India Pvt. Ltd.
  - TEQIP II Sponsored one week Faculty Development Programme on Digital Communication from 19th to 25th June 2015 at BIT, Anna university Trichy
  - One day National level Workshop on Recent trends on Ubiquitous and Embeded Computing on 4th May 2013 at Bharadhidasan Institute of Technology Anna University Regional Centre, Trichy -620 024
  - 2 Weeks ISTE Workshop on Analog Electronics conducted by IIT, Kharagpur, through ICT (MHRD) from 4th June to 14th June 2013
  - One day Workshop on " Recent Trends and application of Nano Photons in Optical Communication on 10th October 2014 at UCEA, Ariyalur – 621 704
  - National Workshop on Analog and Mixed signal design using Cadence Tool from 7th to 8th April 2016 by Karunya University in association with Cadence and Entuple Technologies Bangalore at Karunya University, Coimbatore – 641 114.
  - Attended 18 Webinars 9 Faculty Development Programmes and 1 Short Term Course in ON-Line mode.

- **List of Seminar / Short Term Course /FDP/ Workshop conducted:**

  NIL

- **International / National Conferences Participated:**

  - A paper titled **" FPGA Implementation of an Adaptive Viterbi Decoder for Wireless Applications"** was presented and Won the best paper award in the National Conference on Communication & Information on April 9 – 10, 2010 at Sri Venkateswara College of Engineering, Chennai
  - A paper on **" Efficient Architecture of FM0/Millelr Encoder in ITS Applications using VLSI"** was presented in TEQIP II Sponsored International conference on Emerging Trends in Electrical, Electronics and communication systems ICEEECS'16 at BIT, Anna University, Trichy during 24th to 26th September 2016.
  - Paper titled **" Design & Analysis of Quick Response Low Power STTRAM using POR circuit** has presented in "International Conference on Newer

Engineering Concept & Technology 2K17 (ICONNECT2K17) at K. Ramakrishnan College of Technology, Trichy – 621 112 during 28th to 29th March 2017.

- A Paper titled **" Design & Implementation of Low Power & High Speed Shift Register using FINFET"** had presented in the International Conference on Electrical Information & Communication Technologies (ICEICT2K18) conducted by M.A.M. College of Engineering on 5th April 2018
- Presented a paper entitled "**Enhancement of an Adaptive Automated Warehouse Using Concussion Free Routing Algorithm**" in the AICTE Sponsored International e- Conference on Cutting Edge Technologies in Electrical, Communication, Embedded System and Soft Computing Techniques (ICECES'20) held at Saranathan College of Engineering, Trichy during 05.11.2020 & 06.11.2020

Faculty Development Programmes attended:

S. N.	TITLE	CONDUCTED BY	START DATE	END DATE	LEVEL	No. of Days
1	Analog and Mixed signal design using Cadence Tool	Karunya University, Coimbatore – 641 114	7th April 2016	8th April 2016	National	2 days
2	Recent Trends and application of Nano Photons in Optical Communication	UCEA, Ariyalur – 621 704	10th October 2014	10th October 2014	National	1 day
3	Analog Electronics	IIT, Kharagpur through ICT (MHRD)	4th June 2013	14th June 2013	ISTE	2 weeks
4	Recent trends on Ubiquitous and Embeded Computing	BIT, Anna University Regional Centre, Trichy - 620 024	4th May 2013	4th May 2013	National	1 day

Workshop attended:

S. N.	TITLE	CONDUCTED BY	START DATE	END DATE	Sponsored by	No. of Days
	Digital Communication	BIT, Anna University Regional Centre, Trichy - 620 024	19th June 2015	25th June 2015	TEQIP II	1 week
	Design Techniques of Reconfigurable System	UCE, Pattukkottai – 621 704	26th August 2014	26th August 2014	Enixs Technology India Pvt. Ltd	1 day
	Pedagogy in wireless communication Technologies	M.A.M.C.E.T Trichy - 621 105.	18th November 2013	30th November 2013	AICTE	2 weeks

Publications :

Sl. N.	Title of the Paper	Name of the Journal	Month and Year of Publication	Volume No.	Issue No.	Page No.	ISSN No.	Author Position (in No.)
1	A 4-bit 1.4GS/s Self Calibrated Flash ADC with low Leakage Power in 0.25µm CMOS	Journal of Circuits, Systems,and Computers, IF-0.9	accepted	3	https://doi.org/ 10.1142/ S0218126625502950			3
2	ANALYSIS OF AGING DETECTIONON THE EFFECTIVENESS OF RO BASED SENSOR USING VLSI	GOPALAX JOURNAL	MARCH 2016	8	1	50-56	0976-1345	2

PAPERS PRESENTED IN CONFERENCES:

Sl. No.	Title of the Paper	Name and Venue of the Conference	Date of Presentation	Author Position (in No.)	Level
1	ACCIDENT PREVENTION BASED ON AN AUTOMATIC DETECTIONOF ENERVATIONIN DRIVEWAY	KINGS COLLEGE OF ENGINEERING, PUDUKKOTTAI	MARCH 2020	4	National
2	FPGA IMPLEMENTATION OF AN OPTIMIZED AUDIO DECODER FOR DIGITAL AUDIO BROADCASTING	KINGS COLLEGE OF ENGINEERING, PUDUKKOTTAI	MARCH 2020	4	National
3	AN AREA EFFECTIVE HIGH SPEED SAR DAC ARCHITECTURE USIG 250NM CMOS TECHNOLOGY	SARANATHAN COLLEGEOF ENGINEERING TRICHY	MARCH 2019	4	National
4	WIRELESS UNDERGROUNG SENSOR NETWORKS BASED COAL SAFTY MONITORING SYSTEM	SARANATHAN COLLEGEOF ENGINEERING TRICHY	MARCH 2019	4	National
5	ANALYSISOF JOINTCHANNEL ANDSYMBOL ESATIMATION FOR TWO WAY MIMORELAYING SYSTEMS	SARANATHAN COLLEGEOF ENGINEERING TRICHY	MARCH 2019	4	National
6	HIGH PERFORMANCE5 TO32LINE DECODERS USINGGDI TECHNIQUE	ST JOSEPHS COLLEGE OF ENGINEERING AND TECHNOLOGY, THANJAVUR	MARCH 2019	4	National
7	LOWPOWERAND HIGH SPEED RAP CARRYLOOK AHEADADDER USINGXORXNOR CMOS TECHNOLOGY	ST JOSEPHS COLLEGE OF ENGINEERING AND TECHNOLOGY, THANJAVUR	MARCH 2019	4	National
8	DESIGNAND IMPLEMENTATIONOF LOW POWER ANDHIGHSPEED SHIFTREGISTER USINGFINFET	M.A.M .COLLEGE OF ENGINEERING TRICHIRAPPALLI	APRIL2018	4	National
9	DESIGNAND ANALYSISOF QUICKRESPONSE LOWPOWEER STTRAMUSING NORCIRCUIT	K RAMAKRISHNAN COLLEGE OF TECHNOLOGY TRICHIRAPPALLI	MARCH 2017	4	International
10	EFFICIENT ARCHITECTURE OFEMOMILLER ENCODERINITS APPLICATIONS USINGVLSI	BIT, ANNA UNIVERSITY, TRICHIRAPPALLI	SEPTEMBER 2016	2	International
11	FPGAIMPLEMENTATIONOF AN ADAPTIVE VITERBIDECODERFOR WIRELESS APPLICATIOON	SRI VENKATESWARA COLLEGE OF ENGINEERING CHENNAI	9APRIL 2010	1	National

DECLARATION:

I CERTIFY THAT THE INFORMATION FURNISHED ABOVE IS CORRECT TO THE BEST OF KNOWLEDGE AND BELIEF.

DATE: Signature With date