Name   **: K. VASANTHA**

Designation : TEACHING FELLOW

Mail ID   : vasantha,kumarasamy@gmail.com

Mobile No   : 8940774185

➢ **Area of Interest/Research**: COMMUNICATION SYSTEMS

➢ **Educational Qualifications**:

S.No	Qualification	Specialization	Year of Pass
1	UG	ECE	2005
2	PG	COMMUNICATION SYSTEMS	2010

➢ **Experience:**   **(14 Years)**

Name of Company / Institutions	Designation	Year of Experience
University college of Engineering Ariyalur, Ariyalur	Teaching Fellow	11 Years 4 months
Pavendar Bharathidasan College of Engineering & Technology, Trichy	Assistant Professor	5 months 13 days
Sree Sastha College of Engineering, Chennai	Assistant Professor	2 years, 5 months

➢ **Additional / Academic Responsibilities at Institution/University Level:**
- o Class In-charge
- o Time table In-charge
- o Department Exam cell Co-coordinator
- o Sports Co-coordinator
- o Project Guide
- o Alumni Co-coordinator

➢ **Professional Activities:**

➢ Acted as Examiner for Central valuation of Anna University Chennai.

➢ Acted as External Examiners for various University Practical Examinations

➢ Acted as Internal Examiners in University Theory and Practical Examinations at Anna University of Anna University Chennai.

➢ Acted as Invigilator for TNPSC examination.

➢ Acted as Invigilator for NEET PG 2022 examination

- Produced 100% result in the subject VLSI DESIGN for VI semester of 2015-2016 batch ECE students
- Produced 100% result in the subject LINEAR INTEGRATED CIRCUITS & ITS APPLICATIONS in 2 nd semester of 2013-2014 for EEE students

- **UG/PG Projects Completed: 15**
- **Sponsored Research Activities: NIL**
- **Membership in professional Societies: NIL**
- **List of Seminar / Short Term Course /FDP/ Workshop attended:**

  - AICTE sponsored Faculty Development Programme on Pedagogy in wireless communication Technologies from 18th to 30th November 2013. At M.A.M. College of Engineering & Technology Trichy 621 105.
  - Faculty Development Programme on Design Techniques of Reconfigurable System on 26th August 2014 at University College of Engineering, Pattukkottai in association with Enixs Technology India Pvt. Ltd.
  - TEQIP II Sponsored one week Faculty Development Programme on Digital Communication from 19th to 25th June 2015 at BIT, Anna university Trichy
  - One day National level Workshop on Recent trends on Ubiquitous and Embeded Computing on 4th May 2013 at Bharadhidasan Institute of Technology Anna University Regional Centre, Trichy -620 024
  - 2 Weeks ISTE Workshop on Analog Electronics conducted by IIT, Kharagpur, through ICT (MHRD) from 4th June to 14th June 2013
  - One day Workshop on " Recent Trends and application of Nano Photons in Optical Communication on 10th October 2014 at UCEA, Ariyalur – 621 704
  - National Workshop on Analog and Mixed signal design using Cadence Tool from 7th to 8th April 2016 by Karunya University in association with Cadence and Entuple Technologies Bangalore at Karunya University, Coimbatore – 641 114.
  - Attended 18 Webinars 9 Faculty Development Programmes and 1 Short Term Course in ON-Line mode.

- **List of Seminar / Short Term Course /FDP/ Workshop conducted:**

  NIL

- **International / National Conferences Participated:**

  - A paper titled **" FPGA Implementation of an Adaptive Viterbi Decoder for Wireless Applications"** was presented and Won the best paper award in the National Conference on Communication & Information on April 9 – 10, 2010 at Sri Venkateswara College of Engineering, Chennai
  - A paper on **" Efficient Architecture of FM0/Millelr Encoder in ITS Applications using VLSI"** was presented in TEQIP II Sponsored International conference on Emerging Trends in Electrical, Electronics and communication systems ICEEECS'16 at BIT, Anna University, Trichy during 24th to 26th September 2016.
  - Paper titled **" Design & Analysis of Quick Response Low Power STTRAM using POR circuit** has presented in   "International Conference on Newer Engineering

Concept & Technology 2K17 (ICONNECT2K17) at K. Ramakrishnan College of Technology, Trichy – 621 112 during 28th to 29th March 2017.

➢ A Paper titled **" Design & Implementation of Low Power & High Speed Shift Register using FINFET"** had presented in the International Conference on Electrical Information & Communication Technologies (ICEICT2K18) conducted by M.A.M. College of Engineering on 5th April 2018

➢ Presented a paper entitled "**Enhancement of an Adaptive Automated Warehouse Using Concussion Free Routing Algorithm**" in the AICTE Sponsored International e- Conference on Cutting Edge Technologies in Electrical, Communication, Embedded System and Soft Computing Techniques (ICECES'20) held at Saranathan College of Engineering, Trichy during 05.11.2020 & 06.11.2020

➢ **Publications :**

a. **International Journals:**
A Paper titled, "Analysis Of Aging Detection On The Effectiveness Of Ro Based Sensor Using VLSI" was published in an International Journal of Technology and Engineering System (IJTES) with Vol 8. No.1 – Jan-March 2016 Pp. 50-56 ©gopalax Journals, Singapore ISSN: 0976-1345

b. **National Journals: NIL**

➢ **Conferences Conducted:**

➢ **Invited Lectures.**

➢ **Awards**

➢ **Fellowships and Grants**

➢ **List of Ph.D Research Project Completed**

➢ **List of Ph.D Research Project Ongoing**

➢ **Consultancy Activities**