

Name: G.Prathiba

Designation: Teaching Fellow

Mail ID:prathibagurusamy@gmail.com



➤ **Research Area:** VLSI

➤ **Educational Qualifications:**

Qualification	Department	Year (From -To)	Percentage	Class
Ph.D (Pursuing)	ICE, Anna University, Chennai.	2015-till date (Part Time)	-	-
M.Tech	ECE,PRIST University, Thanjavur.	January 2008 - December 2009	8.85 CGPA	First
BE	ECE,IRTT,Erode	2001-2004	76.95%	Distinction
Diploma	EEE,GPT,Trichy	1998-2001	92.41%	Honours

➤ **Experience: (Years)**

Name of Company / Institutions	Designation of Post	From	To	Experience in Year
		DD-MM-YYYY	DD-MM-YYYY	
UCEA Ariyalur	Teaching Fellow	12.10.2015	Till Date	6.5
PREC,Thanjavur	Asst. Professor	04.01.2010	10.10.2015	5.9
PREC,Thanjavur	Lecturer	26.07.2004	31.12.2007	3.5
Total				15.7

➤ **Additional / Academic Responsibilities at University:**

- Class incharge
- Time table incharge

➤ **Professional Activities:**

➤ **Completed B.E/M.E Projects:**

- 10 BE projects
- 4 PG projects

➤ **Sponsored Research Activities:**

➤ **List of Seminar / Short Term Course / FDP/ Workshop attended: (Within India)**

- Participated in the seminar titled “RESEARCH CULTURE” on 06th January 2012 at PRIST University, Thanjavur.
- Attended a short course titled “PHOTONICS” on 09th December 2006 at PRGI Research and Development wing.
- workshop on 28th August 2004 at P.R.Engineering College, Thanjavur.

➤ **International Conferences Participated:**

- Presented a paper entitled “AN EFFICIENT THRESHOLD TRANSPOSED APPROXIMATION (TTA) ARCHITECTURE OF FLASH ADC IN VLSI” in International Conference On New Trends in Electrical Engineering and Technology (ICNTET’18) September 7th & 8th-2018 at GRT INSTITUTE OF ENGINEERING & TECHNOLOGY, Chennai.
- Presented a paper entitled “EFFICIENT ARCHITECTURE OF FMO/MILLER ENCODER IN ITS APPLICATIONS USING VLSI” in International Conference on Emerging Trends in Electrical Electronics and Communication Systems (ICEEECS’16)September 24 to 26 - 2016 at ANNA UNIVERSITY, Trichy.
- Presented a paper entitled “AUTONOMOUS VISUAL FASTENER MONITERING RAILWAY INSPECTION AND CONTROL SYSTEM” in International Conference on Engineering and Applied Science 28th February 2015 at VETRI VINAYAHA COLLEGE OF ENGG & TECH, Thottiam.
- Presented a paper entitled “TRANSMISSION OF NUMERIC DATA, TEXT AND IMAGE USING LIFI TECHNOLOY” in Third National conference on 21st March 2015 at KONGUNADU COLLEGE OF ENGINEERING AND TECHNOLOGY, Namakalal.

- Presented a paper entitled “AUTOMATIC WAREHOUSE” in National Conference on 16th & 17th October 2014 at VIVEKANANDHA COLLEGE OF ENGINEERING FOR WOMEN, Namakkal.
- Presented a paper entitled “PERFORMANCE EVALUATION OF WIRELESS SENSOR NETWORKS FOR FOREST AND INDUSTRIAL FIRE DETECTION” in National Conference on 14th March 2014 at TEJAA SHAKTHI INSTITUTE OF TECHNOLOGY FOR WOMEN, Coimbatore.
- Participated in the “NATIONAL PROGRAMME ON TECHNOLOGY ENHANCED LEARNING (NPTEL)” workshop on 12th April 2013 at PRIST University, Thanjavur.
- Presented a paper titled “BIOMETRIC VOTING MACHINE” in National Conference on 28th February 2013 at Rover Engineering College, Perambalur.
- Presented a paper entitled “REAL TIME MONITORING OF TEMPERATURE AND VELOCITY USING SELF MOTORIZED SENSOR WITH EFFICIENT HARVESTING SYSTEM” in National Conference on 07th January 2012 at Arasu Engineering College, Kumbakonam.
- Presented a paper titled “DESIGN AND TESTING ANALYSIS OF HIGH SPEED LOW POWER SYSTEM USING VLSI” in National Conference on 06th April 2011 at Arasu Engineering College, Kumbakonam.
- Presented a paper titled “ELEVATING TELEMEDICINE USING GPRS TECHNOLOGY” in National Conference on 29th and 30th March 2006 at Kongu Engineering College, Erode.
- Presented a paper titled “DIGITAL IMAGING AND APPLICATIONS” in National Conference cum workshop on 06th and 07th March 2006 at Adhityamaan College of Engineering, Hosur.

➤ **Publications :**

a. International Journals:

➤ **Research Contribution**

- G.Prathiba and Dr.M.Santhi, “Design and implementation of Reliable Flash ADC for Microwave Applications” Published in Elsevier-Microelectronics Reliability, Volume 88-90, September 2018, Pages 91-97. (<https://doi.org/10.1016/j.microrel.2018.07.095>)
- G.Prathiba and Dr.M.Santhi, “Design of Low Power Fault Tolerant Flash ADC for Instrumentation Applications” Published in Elsevier- Microelectronics Journal, Volume 98, April 2020, 104739 Pages 1-9. (<https://doi.org/10.1016/j.mejo.2020.104739>)

- G.Prathiba, and Dr.M.Santhi,, ‘Design and Analysis of 4-bit 1.2GS/s Low PowerCMOS Clocked Flash ADC” Published in Intelligent Automation and Soft Computing, vol. 31, no. 3, 2022, pp. 1611-1626.([doi:10.32604/iasc.2022.018975](https://doi.org/10.32604/iasc.2022.018975))
- G.Prathiba and Dr.M.Santhi, “A New Structure of 8-Bit 60MS/s SAR-ADC using a Reduced Switching Capacitor-DAC Array” Published in Journal of Circuits, Systems, and Computers, vol. 31, No. 3, 2022, 2250053 (26 pages).(<https://doi.org/10.1142/S0218126622500530>)

➤ **Journals Publication**

- Published a paper titled “**Analysis Of Aging Detection On The Effectiveness Of Ro Based Sensor Using VLSI**” in International Journal of Technology and Engineering System (IJTES) Vol 8. No.1 – Jan-March 2016 Pp. 50-56.
- Published a paper titled “**Security Lock System for Fake Traits Based on Image Processing**” in International Journal of Engineering and Computer Science. Volume 4 Issue2 February 2015(ISSN:2319-7242).
- Published a paper titled “**Novelty Detection in Compressed Surveillance Videos**” in International Journal of Scientific Research and Engineering Studies, Volume 2 Issue 2 February 2015(ISSN:2349-8862).
- Published a paper titled “**Analysis and OSPF Behavior Performance of IPV6 Enterprise Network in Different Environment**” in International Journal of Scientific Research and Engineering Studies, Volume 2 Issue 3 March 2015(ISSN:2349-8862).

➤ **Conferences**

- Presented a paper titled “**An Efficient Threshold Transposed Approximation (TTA) Architecture Of Flash ADC In VLSI**” in the International Conference on New Trends in Electrical Engineering and Technology (ICNTET’18) supported by IEEE on September 7th & 8th,2018 at GRT INSTITUTE OF ENGINEERING & TECHNOLOGY, Chennai.
- Presented a paper titled “**An Area Effective and High Speed SAR-ADC Architecture for Wireless Communication**” in the International Conference (Springer) on Computer

Networks and Inventive Communication Technologies (ICCNCT-2019), organized by RVS Technical Campus during May 23rd and 24th, 2019 at Coimbatore.

DECLARATION:

I CERTIFY THAT THE INFORMATION FURNISHED ABOVE IS CORRECT TO THE BEST OF KNOWLEDGE AND BELIEF.

DATE:

Signature With date